

Figure 1.

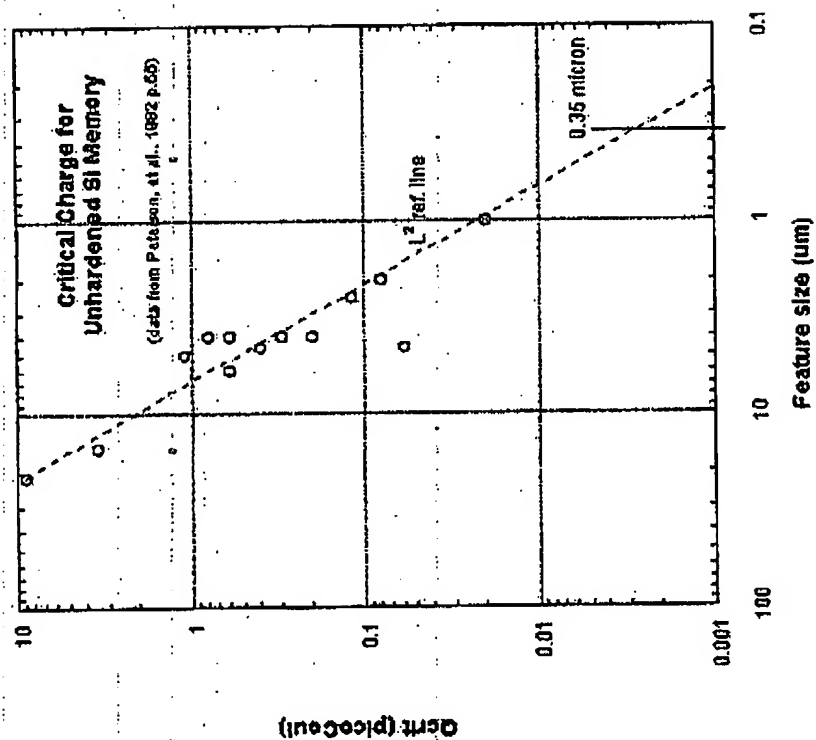
RRW RRWPJM PJMHLH HLHRAA RAA

Figure 2.

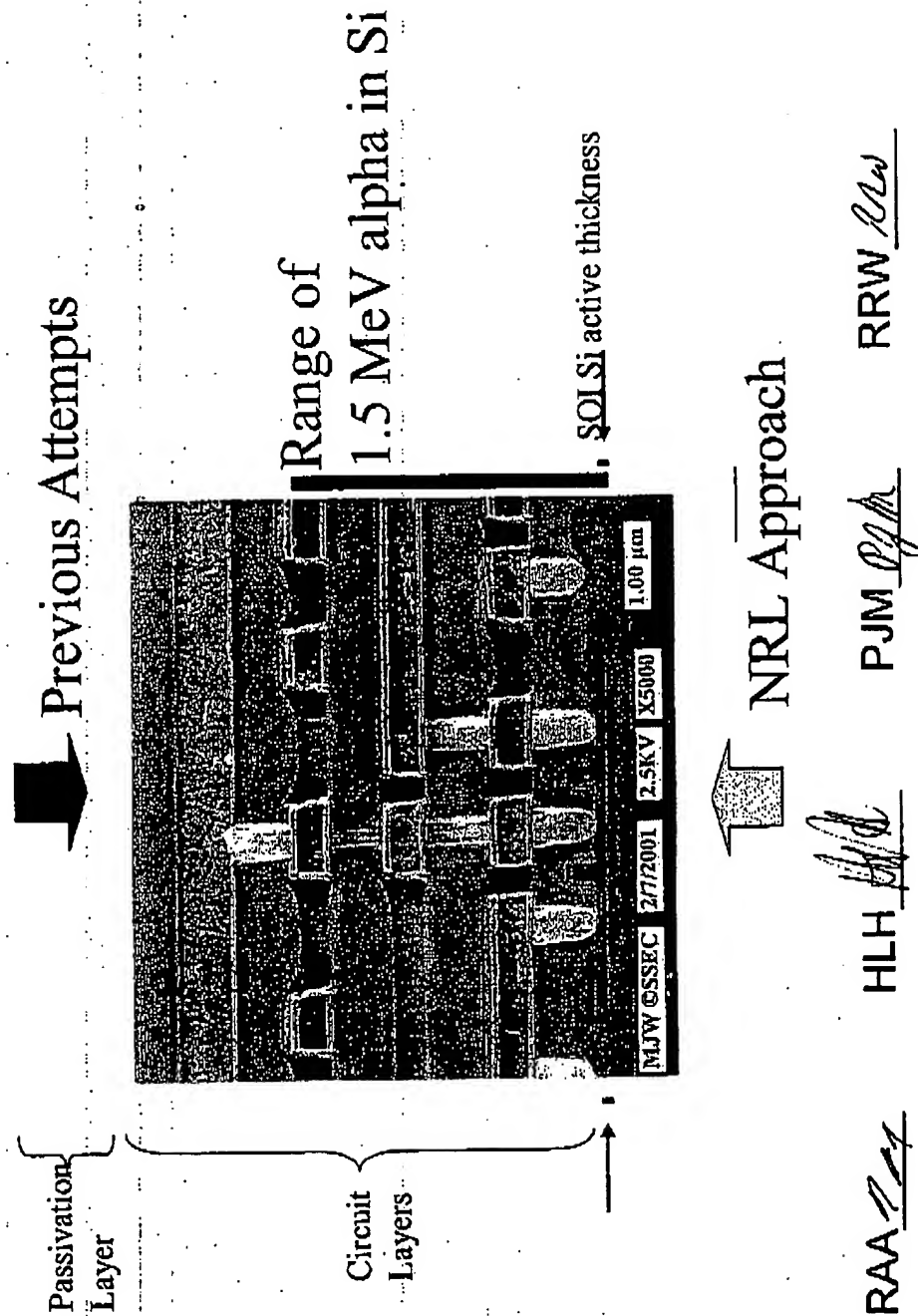
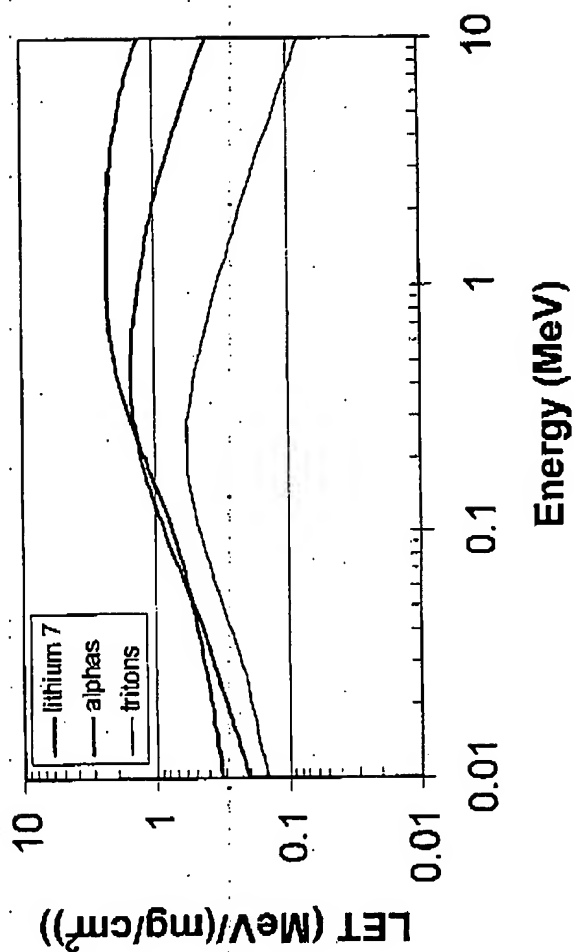


Figure 3.

LET on Silicon



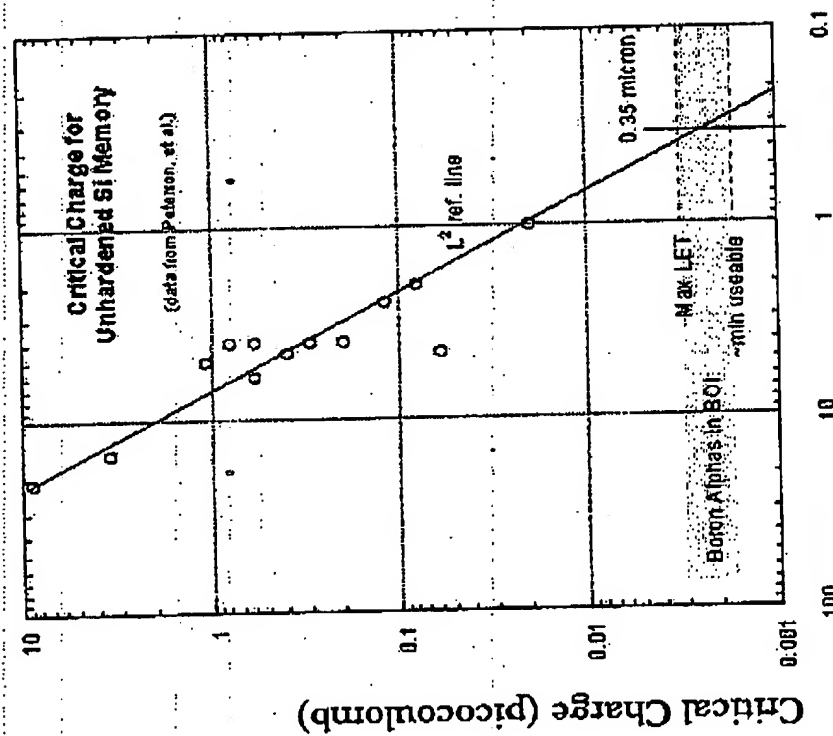
RRW

PJM

HLH

RAA

Figure 4.



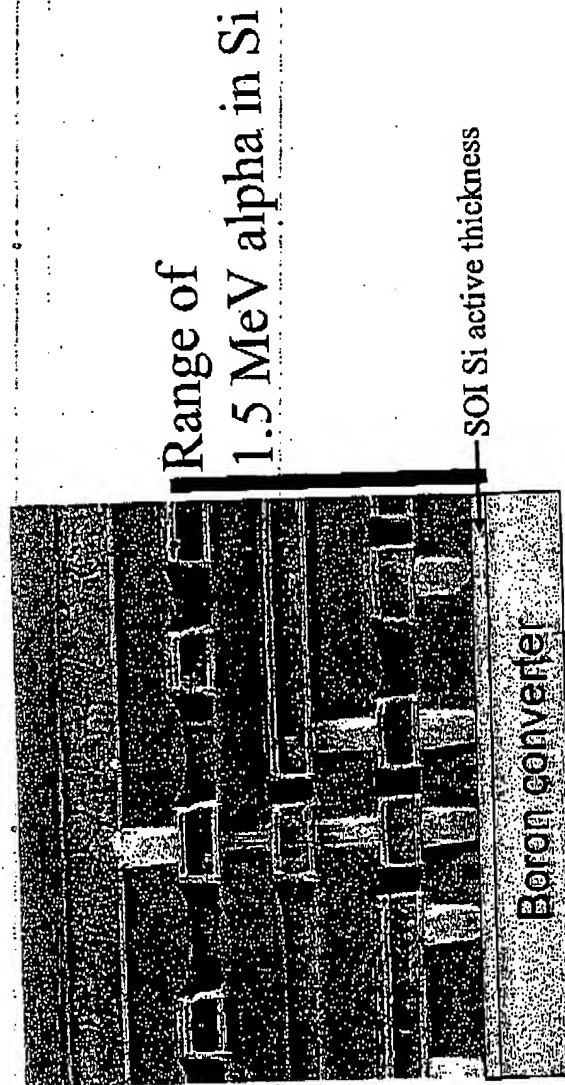
RRW RRW

PJM PJM

HLH HLH

RAA RAA

Figure 5.



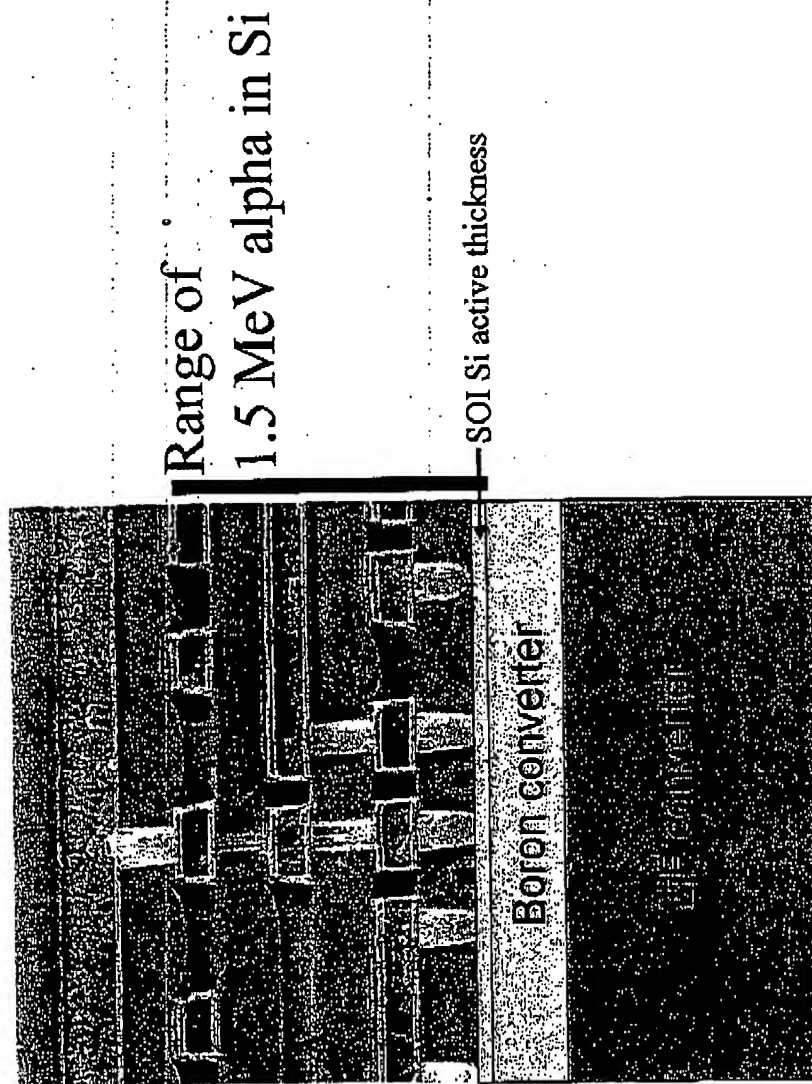
RRW RRW

PJM PJM

HLH HLH

RAA RAA

Figure 6.

RRW RRWPJM PJMHLH HLHRAA RAA